

2173  
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Attorney's Docket No. 42390P11912

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Michal J. Cierniak

Application No.: 09/961,048

Filed: September 20, 2001

For: METHOD FOR IMPLEMENTING MULTIPLE  
TYPE HIERARCHIES

)  
) Art Unit: 2173  
)  
)  
) Examiner: Unassigned  
)  
)  
)

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APR 09 2002

Technology Center 2100

Assistant Commissioner for Patents  
Washington, D.C. 20231

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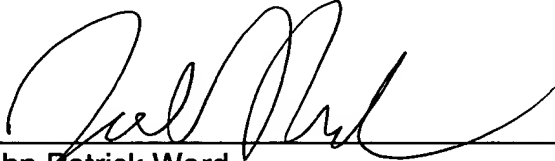
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/3, 2002

  
\_\_\_\_\_  
John Patrick Ward  
Registration No. 40,216

12400 Wilshire Blvd.  
Seventh Floor  
Los Angeles, CA 90025-1026  
(408) 720-8300

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

APR 08 2002

Application Number	09/961,048
Filing Date	September 20, 2001
First Named Inventor:	Michal J. Cierniak
Group Art Unit	2173
Examiner Name	Not yet assigned
Attorney Docket Number	42390P11912

Sheet 1 of 1

## OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		GOSLING, J., et al., The Java™ Language Specification, Second Edition, Addison-Wesley, 1999, Cover Page-505 pp.	
		LINDHOLM, T., et al., The Java Virtual Machine Specification, Second Edition, Addison-Wesley, 1999, 302 pp.	
		ECMA, C# Language Specification, Standard ECMA-334, December 2001, Cover Page -479 pp.	
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Serial/Patent No.: 09/961,048

Filing/Issue Date: September 20, 2001

Client: Intel Corporation

Title: Method for Implementing Multiple Type Hierarchies

BSTZ File No.: 042390.P11912

Atty/Secty Initials: EHT/JPW/jxs/bks

Date Mailed: April 3, 2002

Docket Due Date: \*\*\*

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